

液晶模组说明书

LCD Module Instructions

☒初定规格 Preliminary specification

☐正式规格 Official specifications

项目编号 Project No.	TFT-H078B06
产品描述 Product Description	TFT LCD Module 400 (RGB) x 1280 Pixels 7.8 Inch TFT LCD
客 户 确 认 签 章: Signature by customer:	
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HotDisplay Technology Co., Ltd

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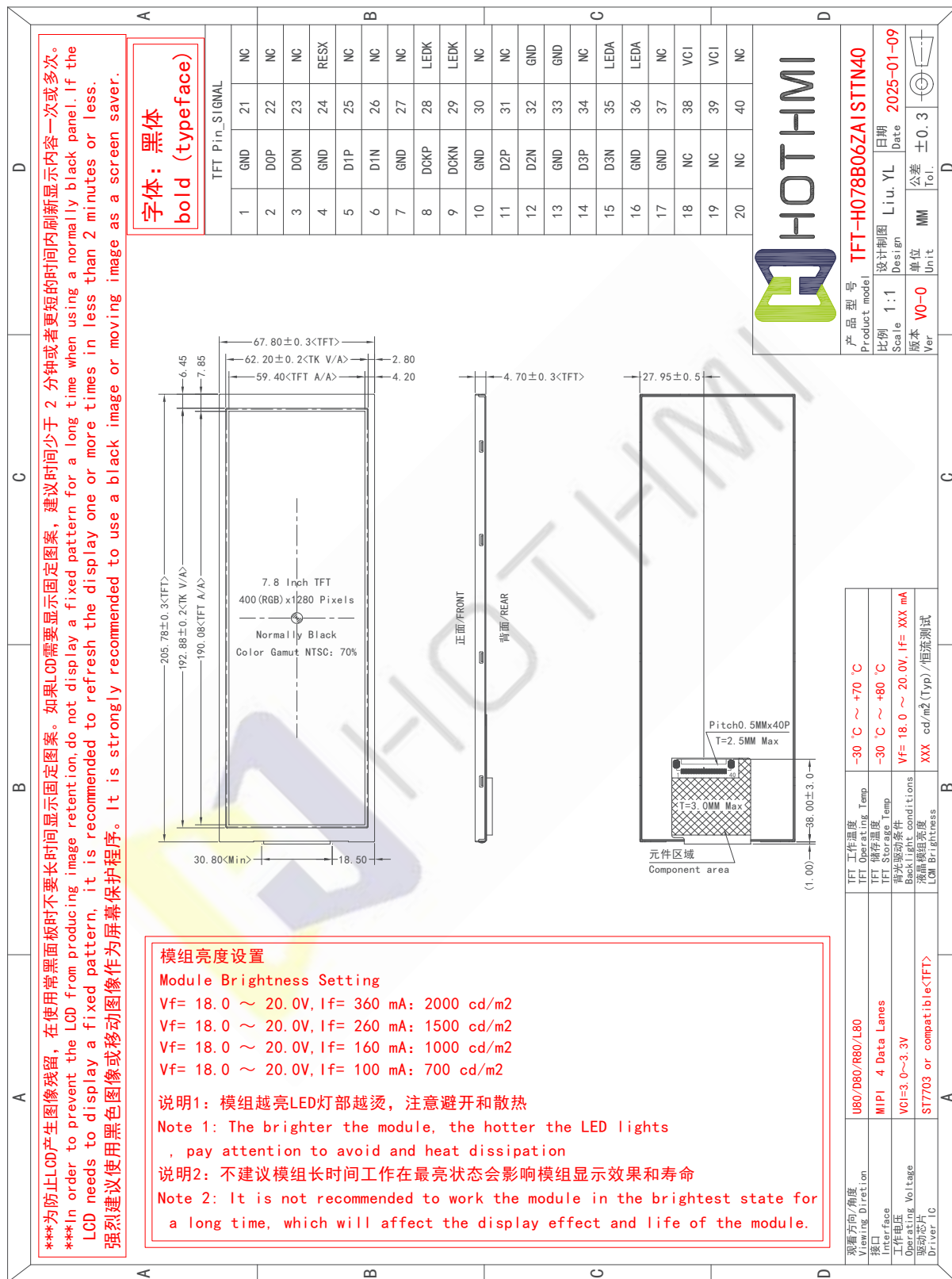
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一、基本特征 General Feature:

项目 Item	标准值 Standard Value			单位 Unit
TFT 显示尺寸 TFT Display Size	7.8			英寸 Inch
TFT 分辨率 TFT Number of Pixels	400 (RGB) (H) x 1280 (V)			像素 Pixel
TFT 显示有效区域 TFT Display Active Area	59.40 (H) x 190.08 (V)			毫米 mm
TFT 模组外形尺寸 TFT Module Dimensions	67.80 (H) x 205.78 (V) x 4.70 (D) <TFT> 67.80 (H) x 205.78 (V) x 6.40 (D) <TFT+CTP>			毫米 mm
TFT 观看方向 TFT Viewing Direction	U80/D80/R80/L80 (Typ.)			度 Deg.
TFT 模组接口 TFT Module Interfaces	MIPI 4 Data Lanes			—
TFT 驱动芯片 TFT Driver IC	ST7703 or compatible			—
电容触摸屏驱动芯片 CTP Driver IC	FT7511			—
触摸屏选项 Touch Screen Options	☒ 不带触摸屏 Whitout TP	☐ 带电阻触摸屏 With RTP	☒ 带电容触摸屏 With CTP	—
TFT 模组重量 TFT Module Weight	不带触摸屏 Without TP	T. B. D ±3%		克 g
	带电容触摸屏 With CTP	T. B. D ±3%		
	带电阻触摸屏 With RTP	T. B. D ±3%		

注释 Note:

二、外形尺寸 Outline Dimensions



TFT-H078B06ZA1STTN40_V0-0 (NTP_XXX cd/m²)



HOT-HMI

Shenzhen Hot Display Technology Co., Ltd

TFT-H078B06

2025 年 01 月 17 日
Rev:0-0

字体：黑体
bold (typeface)

TFT Pin_SIGNAL

1	GND	21	NC
2	D0P	22	NC
3	D0N	23	NC
4	GND	24	RESX
5	D1P	25	NC
6	D1N	26	NC
7	GND	27	NC
8	DCKP	28	LEDK
9	DCKN	29	LEDK
10	GND	30	NC
11	D2P	31	NC
12	D2N	32	GND
13	GND	33	GND
14	D3P	34	NC
15	D3N	35	LEDA
16	GND	36	LEDA
17	GND	37	NC
18	NC	38	VC1
19	NC	39	VC1
20	NC	40	NC



HOT-HMI

产品型号
Product model

TFT-H078B06ZA1STTC40

比例
Scale

1:1

设计制图
Design

Liu, YL

日期
Date

2025-01-09

版本
Ver

V0-1

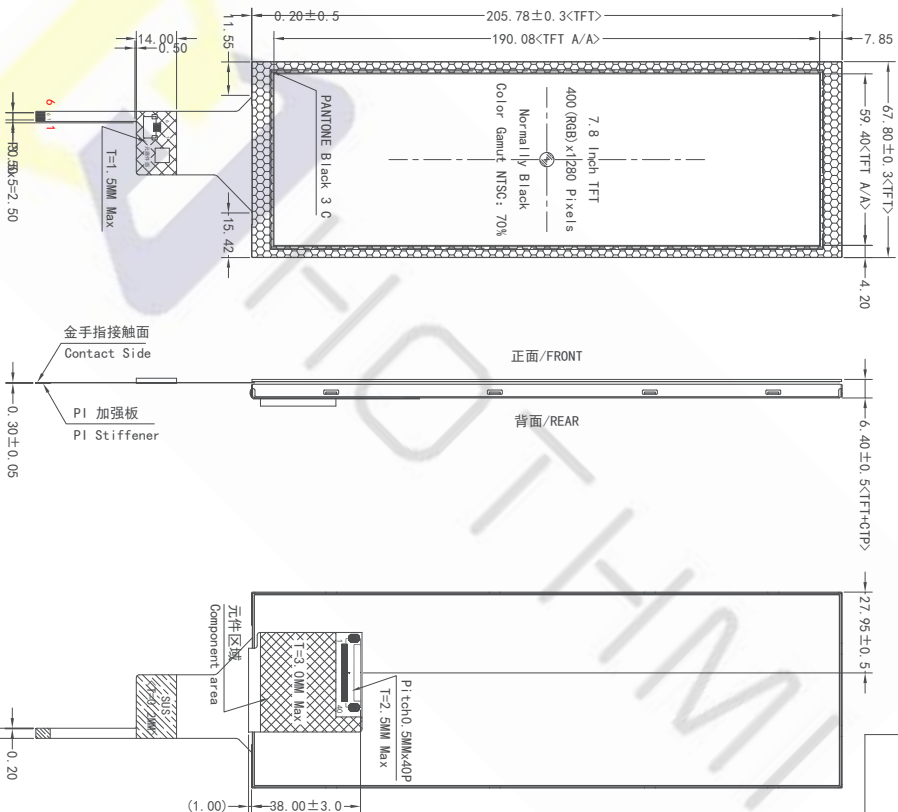
单位
Unit

MM

公差
Tol.

±0.3

***为防止LCD产生图像残留，在使用常黑面板时不要长时间显示固定图案。如果LCD需要显示固定图案，建议时间少于 2 分钟或者更短的时间内刷新显示内容一次或多次。
***In order to prevent the LCD from producing image retention, do not display a fixed pattern for a long time when using a normally black panel. If the LCD needs to display a fixed pattern, it is recommended to refresh the display one or more times in less than 2 minutes or less.
强烈建议使用黑色图像或移动图像作为屏幕保护程序。It is strongly recommended to use a black image or moving image as a screen saver.



模组亮度设置
Module Brightness Setting

Vf= 18.0 ~ 20.0V, If= 360 mA; 1700 cd/m²
Vf= 18.0 ~ 20.0V, If= 260 mA; 1250 cd/m²
Vf= 18.0 ~ 20.0V, If= 160 mA; 850 cd/m²
Vf= 18.0 ~ 20.0V, If= 100 mA; 600 cd/m²

说明1：模组点亮LED灯部越亮，注意避开和散热

说明2：不建议模组长时间工作在最亮状态会影响模组显示效果和寿命

Note 2: It is not recommended to work the module in the brightest state for a long time, which will affect the display effect and life of the module.

CTP_SIGNAL	1	2	3	4	5	6
SCL	SDA	VDD	RTN	INT	GND	

观看方向/角度 Viewing Direction	U80/D80/R80/L80	TFT 工作温度 TFT Operating Temp	-30 °C ~ +70 °C
接口 Interface	MIPI 4 Data Lanes<TFT>, IIC<CTP>	TFT 储存温度 TFT Storage Temp	-30 °C ~ +80 °C
工作电压 Operating Voltage	VDD = VC1 = 3.0~3.3V	背光驱动条件 Backlight conditions	Vf= 18.0 ~ 20.0V, If= XXX mA
驱动芯片 Driver IC	ST7703 or compatible<TFT>, FT7511<CTP>	液晶模组亮度 LCM Brightness	XXX cd/m ² (Typ) / 恒流测试

TFT-H078B06ZA1STTC40_V0-1 (NTP_XXX cd/m²)

三、引脚说明 Pin Description

3.1 模组引脚说明 TFT Pin Description

引脚编号 Pin NO.	标号 Symbol	详细描述 Description
1	GND	电源地 Power supply ground
2	D0P	MIPI 数据通道 0 正端输入引脚 MIPI data Lane 0 positive-end input pin
3	D0N	MIPI 数据通道 0 负端输入引脚 MIPI data Lane 0 negative-end input pin
4	GND	电源地 Power supply ground
5	D1P	MIPI 数据通道 1 正端输入引脚 MIPI data Lane 1 positive-end input pin
6	D1N	MIPI 数据通道 1 负端输入引脚 MIPI data Lane 1 negative-end input pin
7	GND	电源地 Power supply ground
8	DCKP	LVDS 时钟线正端输入引脚 LVDS clock Lane positive-end input pin
9	DCKN	LVDS 时钟线负端输入引脚 LVDS clock Lane negative-end input pin
10	GND	电源地 Power supply ground
11	D2P	MIPI 数据通道 2 正端输入引脚 MIPI data Lane 2 positive-end input pin
12	D2N	MIPI 数据通道 2 负端输入引脚 MIPI data Lane 2 negative-end input pin
13	GND	电源地 Power supply ground
14	D3P	MIPI 数据通道 3 正端输入引脚 MIPI data Lane 3 positive-end input pin
15	D3N	MIPI 数据通道 3 负端输入引脚 MIPI data Lane 3 negative-end input pin
16~17	GND	电源地 Power supply ground
- 下一页 - -Next page -		

第 6 页 共 27 页

引脚编号 Pin NO.	标号 Symbol	详细描述 Description
18~23	NC	空引脚, 未连接 Empty Pin, Unconnected
24	RESX	重置信号。信号为低电平有效 Reset signal. Signal is active low
25~27	NC	空引脚, 未连接 Empty Pin, Unconnected
28~29	LEDK	LED阴极 LED cathode
30~31	NC	空引脚, 未连接 Empty Pin, Unconnected
32~33	GND	电源地 Power supply ground
34	NC	空引脚, 未连接 Empty Pin, Unconnected
35~36	LEDA	LED阳极 LED anode
37	NC	空引脚, 未连接 Empty Pin, Unconnected
38~39	VCI	电源 Power supply
40	NC	空引脚, 未连接 Empty Pin, Unconnected
- 结束 - - END -		

3.2 电容触摸引脚说明 CTP Pin Description

引脚编号 Pin NO.	标号 Symbol	详细描述 Description
1	SCL	CTP IIC时钟信号 CTP IIC clock signal
2	SDA	CTP IIC数据信号 CTP IIC data signal
3	VDD	CTP 电源 CTP Power supply
4	RTN	CTP 系统复位 CTP System reset
5	INT	CTP 中断信号 CTP Interrupt signal
6	GND	电源地 Power supply ground
- 结束 - - END -		

3.3 参考电路图 Reference Circuit Diagram



Title		鑫洪泰科技（广东）有限公司	
Size	Number	Revision	
	B	TFT-H078B06-接口参考电路	
Date:		V0	
File:		Sheet of	

四、电气特性 Electrical Characteristics

4-1 TFT 面板工作条件 TFT Panel Operating Conditions

项目 Item	标号 Symbol	条件 Condition	最小值 Min	典型值 Type	最大值 Max	单位 Unit
数字电源 Digital Power	VCI	-	2.8	-	3.3	伏 V
接口工作电压 IO Supply Voltage	IOVCC	-	2.8	-	3.3	伏 V
工作电流 Operation Current	I _{oc}	-	-	-	-	毫安 mA
待机电流 Standby Current	I _{sc}	-	-	-	-	微安 uA
TFT 工作温度 TFT Operating Temp	TOPR	-	-30	25	+70	摄氏度 ℃
TFT 储存温度 TFT Storage Temp	TSTG	-	-30	25	+80	摄氏度 ℃

4-3 电容触摸屏工作条件 CTP Operating Conditions

项目 Item	标号 Symbol	条件 Condition	最小值 Min	典型值 Type	最大值 Max	单位 Unit
表面硬度 Surface hardness	-	铅笔 Pencil	-	6H	-	硬度 Hard
透光率 Transmittance	-	-	80	-	-	百分比 %
数字电源 Digital Power	VDD	-	2.8	3.30	3.60	伏特 V
ESD 保护电压 ESD Protection	-	HB Model	-	-	2000	伏 V
工作温度 Operation Temperature	TOPR	20%-90%RH;	-30	25	+80	摄氏度 ℃
储存温度 Storage Temperature	TSTG		-30	25	+80	摄氏度 ℃

4-4 背光工作条件 LED back light specification

Backlighting 1:

项目 Item	标号 Symbol	条件 Condition	最小值 Min	典型值 Type	最大值 Max	单位 Unit
工作电压 Forward voltage	VF	恒流 constant current	18.0	-	20.0	伏特 V
工作电流 Forward current	IF		-	-	360	毫安 mA
亮度 (带 LCD) Luminance (With LCD)	Lv	不带触摸 Without TP	-	-	2000	坎德拉/平方米 cd/m ²
		带触摸 With TP	-	-	1700	
LED 寿命 LED life time	Hr	Ta=25±3 °C	20,000	30,000	-	小时 Hour

Backlighting 2:

项目 Item	标号 Symbol	条件 Condition	最小值 Min	典型值 Type	最大值 Max	单位 Unit
工作电压 Forward voltage	VF	恒流 constant current	-	-	-	伏特 V
工作电流 Forward current	IF		-	-	-	毫安 mA
亮度 (带 LCD) Luminance (With LCD)	Lv	不带触摸 Without TP	-	-	-	坎德拉/平方米 cd/m ²
		带触摸 With TP	-	-	-	
LED 寿命 LED life time	Hr	Ta=25±3 °C	20,000	30,000	-	小时 Hour

注释 Note:

1. LED 寿命 (Hr) 定义为在 Ta=25±3 °C, 上表所示的典型电压电流值条件下持续工作直至亮度低于 50% 的时间。

LED life time (Hr) can be defined as the time in which it continues to operate under the condition: Ta=25±3 °C, typical IL value indicated in the above table until the brightness becomes less than 50%.

2. 以上结果是按 MTBF 计算方式预估判定的 LED 失效时间, 实际测试 LED 在 Ta=25±3 °C 点亮 5000H, 亮度衰减 8%.

The above results are estimated and judged by the MTBF calculation method of the LED failure time. The actual test LED is lit for 5000H at Ta=25±3 °C, and the brightness decays by 8%.

五、液晶光学规格 TFT OPTICAL SPECIFICATION

光学规格的测试应在暗室（环境亮度 1lux，温度=25 ℃）中使用亮度计系统（测角仪系统和TOPCON BM-5）设备进行测量，测试单元应位于大约在 θ 和 Φ 等于 0 的视角下，距 LCD 表面 50cm 的距离。显示面上测量点的中心应保持固定。测量前背光应工作 30 分钟。

The test of Optical specifications shall be measured in a dark room (ambient luminance 1lux and temperature = 25 ℃) with the equipment of Luminance meter system (Goniometer system and TOPCON BM-5) and test unit shall be located at an approximate distance 50cm from the LCD surface at a viewing angle of θ and Φ equal to 0 . The center of the measuring spot on the Display surface shall stay fixed. The backlight should be operating for 30 minutes prior to measurement.

5.2 光学规格 Optical Specifications

参数 Parameter		标号 Symbol	条件 Condition	最小值 Min.	典型值 Typ.	最大值 Max.	单位 Unit	备注 Remark
视角范围 Viewing Angle Range	水平 Horizontal	⊙ 左/L	CR>10	70	80	—	Deg.	Note 1
		⊙ 右/R		70	80	—	Deg.	
	垂直 Vertical	⊙ 上/U		70	80	—	Deg.	
		⊙ 下/D		70	80	—	Deg.	
对比度 Contrast ratio		CR	⊙ = 0°	700	900	—	—	Note2
色域 Color Gamut		CG	CIE1931	65	70	—	%	
白色色度 White Chromaticity		Wx	⊙ = 0°	-0.03	0.300	+0.03	—	Note4 (Based on C Light)
		Wy			0.329		—	
色彩还原 Reproduction of color	红 Red	Rx			—		—	
		Ry			—		—	
	绿 Green	Gx			—		—	
		Gy			—		—	
	蓝 Blue	Bx			—		—	
		By			—		—	
响应时间（上升 + 下降） Response Time (Rising + Falling)		Tr+Tf	⊙ = 0° Ta= 25℃	—	30	40	ms	Note5

注释 Note:

1. 视角是对比度大于10的角度。视角确定为相对于光轴的水平或3、9点钟方向和垂直或6、12点钟方向 垂直于 LCD 表面（见图 1）。

Viewing angle is the angle at which the contrast ratio is greater than 10. The viewing angles are determined for the horizontal or 3, 9 o' clock direction and the vertical or 6, 12 o' clock direction with respect to the optical axis which is normal to the LCD surface (see FIGURE 1).

2. 对比度测量应在 $\theta = 0$ 的视角和 LCD 表面的中心进行。亮度测量时，视场中的所有

像素首先设置为白色，然后设置为暗（黑色）状态。（参见图 1）亮度对比度（CR）是通过数学定义的。

Contrast measurements shall be made at viewing angle of $\Theta = 0$ and at the center of the LCD surface. Luminance shall be measured with all pixels in the view field set first to white, then to the dark (black) state. (see FIGUR 1) Luminance Contrast Ratio (CR) is defined mathematically.

$$CR = \frac{\text{Luminance when displaying a white raster}}{\text{Luminance when displaying a black raster}}$$

3. 透射率是没有 APF 和没有 CG 的值。

Transmittance is the Value without APF and without CG.

4. 上表中规定的色度坐标应由所有像素首先测量的光谱数据计算为红色、绿色、蓝色和白色。测量应在面板的中心进行。

The color chromaticity coordinates specified in the above table shall be calculated from the spectral data measured with all pixels first in red, green, blue and white. Measurements shall be made at the center of the panel.

5. 电光响应时间测量应如图 2 所示，通过打开和关闭“数据”输入信号来进行。亮度从 10%变化到90%所需的时间是 T_r ，90%到10%是 T_f 。

The electro-optical response time measurements shall be made as FIGURE 2 by switching the “data” input signal ON and OFF. The times needed for the luminance to change from 10% to 90% is T_r , and 90% to 10% is T_f .

Figure1 Measurement Set Up

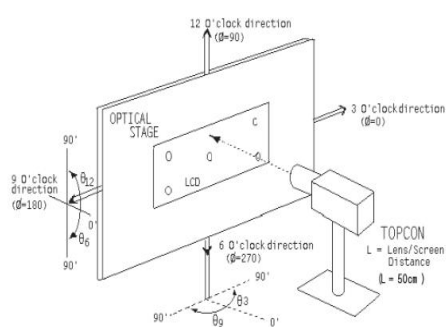


图 1

Optical
response

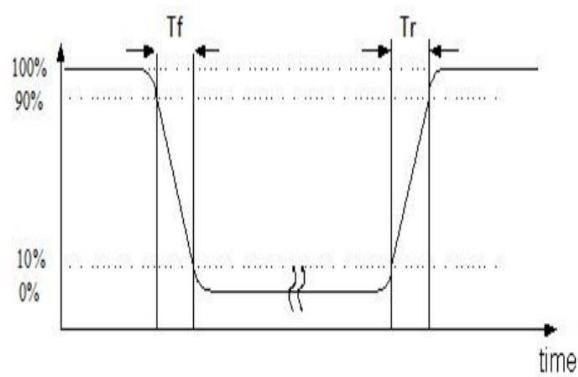


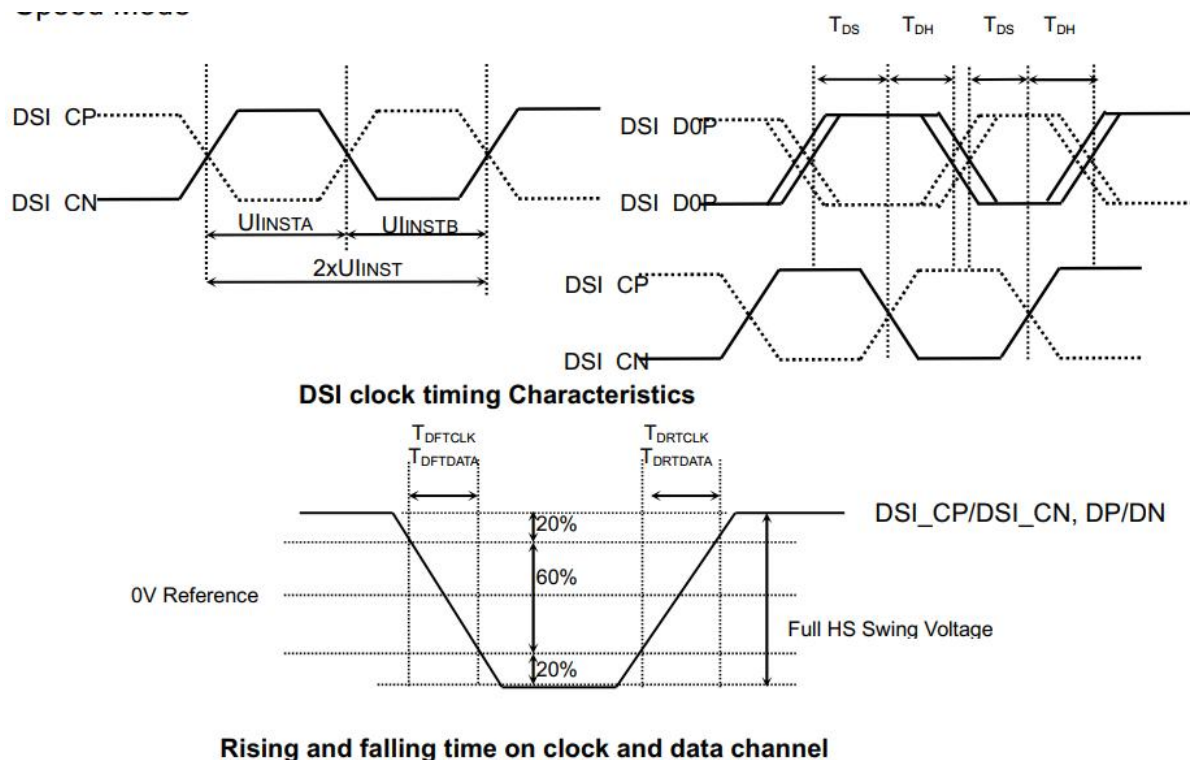
图 2

六、交流特性 AC characteristic

6.1. 时序 Timing

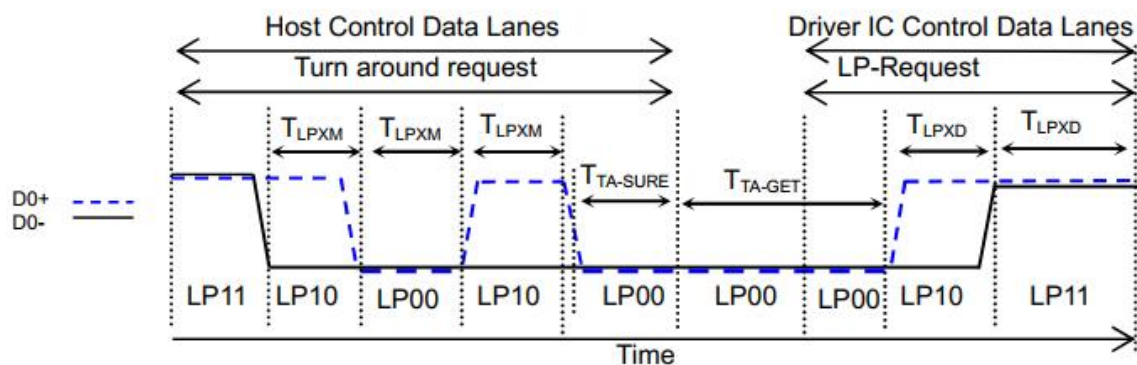
6-1 DSI 接口时序特性 / DSI Interface Timing Characteristics

A. 高速模式 High Speed Mode

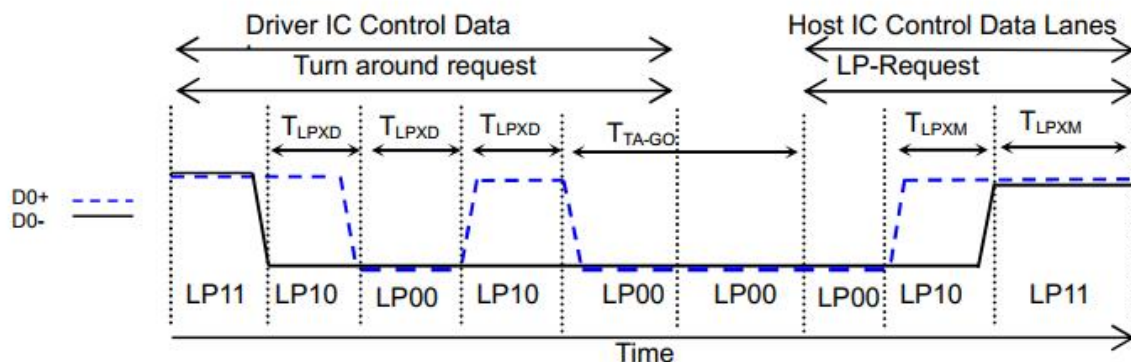


Signal	Item	Symbol	Spec.			Unit
			Min.	Typ.	Max.	
DSI_CP/ DSI_CN	Double UI instantaneous	2xUIINST	4LANE: 3.30 3LANE: 2.85 @ VDDD=1.8V	-	25	ns
	UI instantaneous	UIINSTA UIINSTB	4LANE: 1.67 3LANE: 1.43 @ VDDD=1.8V	-	12.5	ns
DP/DN	Data to clock setup time	T _{DS}	0.15xUI	-	-	ps
	Data to clock hold time	T _{DH}	0.15xUI	-	-	ps
DSI_CP/ DSI_CN	Differential rise time for clock	T _{DRTCLK}	150	-	0.3UI	ps
	Differential fall time for clock	T _{DFTCLK}	150	-	0.3UI	ps
DP/DN	Differential rise time for data	T _{DRTDATA}	150	-	0.3UI	ps
	Differential fall time for data	T _{DFTDATA}	150	-	0.3UI	ps

B. 低功耗模式 Low Power Mode



BTA from HOST to Display Module Timing

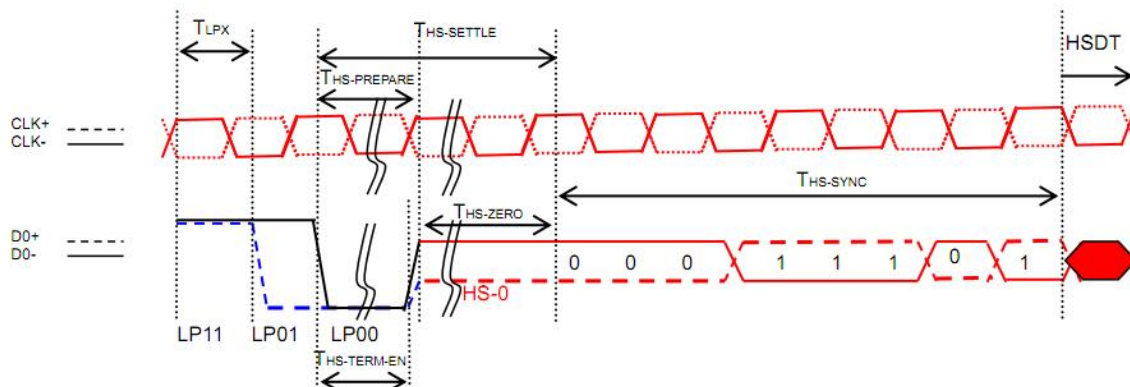


BTA from Display Module Timing to HOST

Signal	Item	Symbol	Spec.			Unit
			Min.	Typ.	Max.	
DSI_D0P/ DSI_D0P	Length of LP-00/LP01/LP10/LP11 Host → Display module	T_{LPXM}	50	-	-	ns
	Length of LP-00/LP01/LP10/LP11 Display module → Host	T_{LPXD}	50	-	-	ns
	Time-out before the MPU start driver	$T_{TA-SURE}$	T_{LPXD}	-	$2 \times T_{LPXD}$	ns
	Time to drive LP-00 by display module	T_{TA-GET}	$5 \times T_{LPXD}$	-	-	ns
	Time to drive LP-00 after turnaround request Host	T_{TA-GO}	$4 \times T_{LPXD}$	-	-	ns

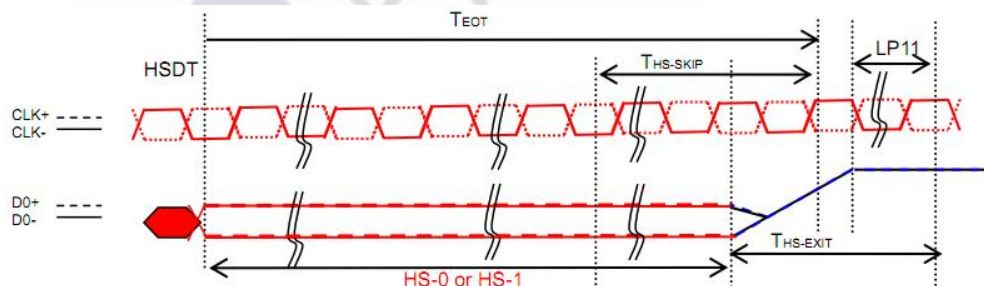
DSI Low Power Mode Characteristics

C. DSI 转换 DSI BURSTS



Signal	Item	Symbol	Spec.			Unit
			Min.	Typ.	Max.	
DSI_D0P/ DSI_D0P	Length of LP-00/LP01/LP10/LP11	TLPX	50	-	-	ns
	Time to Driver LP-00 to prepare for HS transmission	THS-PREPARE	40+4UI	-	85+6UI	ns
	Time to enable data receiver line termination	THS-TERM-EN	-	-	35+4xUI	ns
	Time to drive LP-00 by display module	T _{TA-GET}	5xTLPXD	-	-	ns
	Time to drive LP-00 after turnaround request Host	T _{TAGO}	4xTLPXD	-	-	ns

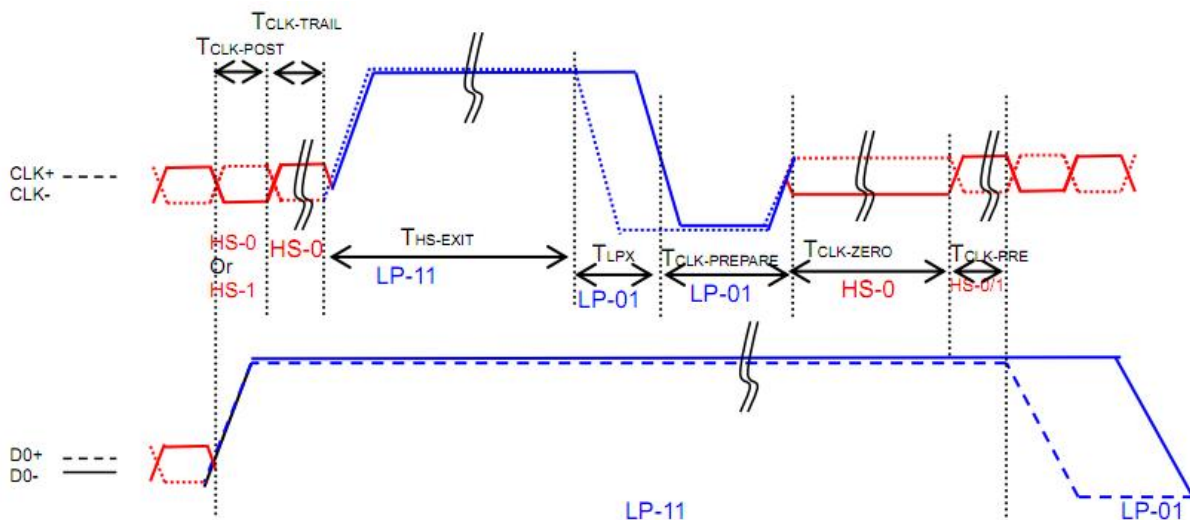
Table 6.3: DSI Low Power Mode to High Speed Mode Timing



NOTE:
 If the last bit is HS-0, the transmitter changes from HS-0 to HS-1
 If the last bit is HS-1, the transmitter changes from HS-1 to HS-0

Signal	Item	Symbol	Spec.			Unit
			Min.	Typ.	Max.	
DSI_D0P/ DSI_D0P	Time-Out at Display Module to Ignore Transition Period of EoT	T _{HS-SKIP}	40	-	55+4xUI	ns
	Time to Driver LP-11 after HS Burst	T _{HS-EXIT}	100	-	-	ns

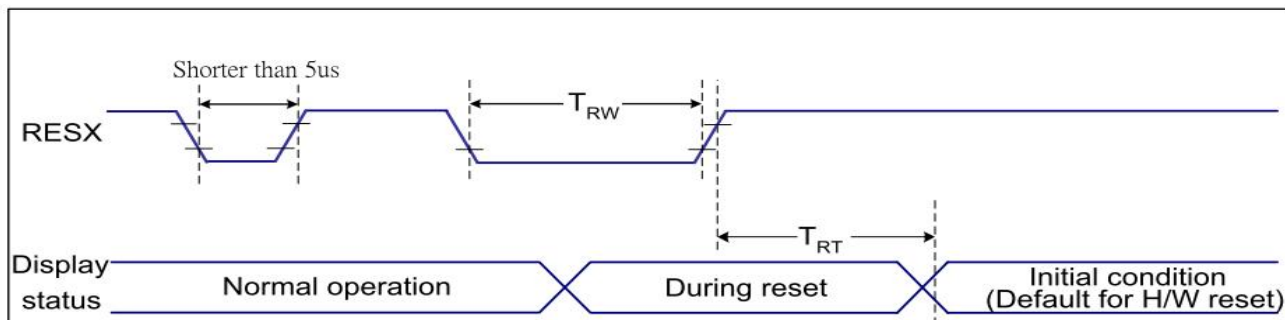
Table 6.4: DSI Low Power Mode to High Speed Mode Timing



Signal	Item	Symbol	Spec.			Unit
			Min.	Typ.	Max.	
DSI_CP/ DSI_CN	Time that the MCU shall continue sending HS clock after the last associated Data Lane has transitioned to LP mode	T _{CLK-POST}	60+52xUI	-	-	ns
	Time to drive HS differential state after last payload clock bit of a HS transmission burst	T _{CLK-TRAIL}	60	-	-	ns
	Time to drive LP-11 after HS burst	T _{HS-EXIT}	100	-	-	ns
	Time to drive LP-00 to prepare for HS transmission	T _{CLK-PREPARE}	38	-	95	ns
	Time-out at Clock Lane Display Module to enable HS Termination	T _{CLK-TERM-EN}	-	-	38	ns
	Minimum lead HS-0 drive period before starting Clock	T _{CLK-PREPARE} + T _{CLK-ZERO}	300	-	-	ns
	Time that the HS clock shall be driven prior to any associated data Lane beginning the transition from LP to HS mode	T _{CLK-PRE}	8xUI			

Table 6.5: Clock Lanes High Speed Mode to/from Low Power Mode Timing

6.2. 复位时序 Reset Timing



VDDI=1.8, VDD=2.8, AGND=DGND=0V, Ta=25 °C

Related Pins	Symbol	Parameter	MIN	MAX	Unit
RESX	TRW	Reset pulse duration	10	-	us
	TRT	Reset cancel	-	5 (Note 1, 5)	ms
				120 (Note 1, 6, 7)	ms

注释 Notes:

1. 复位取消还包括将 ID 字节、VCOM 设置和其他设置从 NVM（或类似设备）加载到寄存器所需的时间。每次在 RESX 上升沿后 5 ms 内有硬件复位取消时间（tRT）时，都会执行此加载。

The reset cancel includes also required time for loading ID bytes, VCOM setting and other settings from NVM (or similar device) to registers. This loading is done every time when there is HW reset cancel time (tRT) within 5 ms after a rising edge of RESX.

2. 根据下表，由于 RESX 线路上的静电放电引起的尖峰不会导致不规则的系统复位

Spike due to an electrostatic discharge on RESX line does not cause irregular system reset according to the table below:

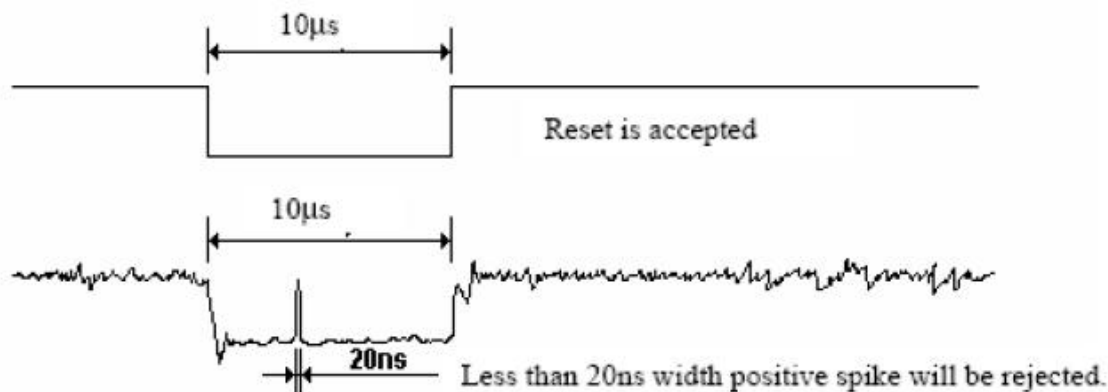
RESX Pulse	Action
Shorter than 5us	Reset Rejected
Longer than 9us	Reset
Between 5us and 9us	Reset starts

3. 在复位期间，显示屏将被消隐（显示屏进入消隐序列，最长持续时间为 120 毫秒，此时 Reset Starts in Sleep Out - mode。显示屏在 Sleep In - mode 中保持消隐状态。）然后返回硬件复位的默认条件。

During the Resetting period, the display will be blanked (The display is entering blanking sequence, which maximum time is 120 ms, when Reset Starts in Sleep Out - mode. The display remains the blank state in Sleep In - mode.) and then return to Default condition for Hardware Reset.

4. 尖峰抑制也适用于有效复位脉冲期间，如下所示：

Spike Rejection also applies during a valid reset pulse as shown below:



5. 在进入睡眠期间应用复位时

When Reset applied during Sleep In Mode.

6. 在退出睡眠期间应用复位时

When Reset applied during Sleep Out Mode.

7. 释放 RESX 后需要等待 5 毫秒以上才能发送命令。也不能在 120 毫秒内发送睡眠退出命令。

It is necessary to wait 5msec after releasing RESX before sending commands. Also Sleep Out command cannot be sent for 120msec.

6.3. 开机上电/掉电顺序 Power-on/Off sequence

IOVDD 和 VDD 可以按任何顺序应用。

IOVDD and VDD can be applied in any order.

VDD 和 IOVDD 可以按任何顺序断电。

VDD and IOVDD can be power down in any order.

在关机期间, 如果 LCD 处于睡眠模式, VDD 和 IOVDD 必须在 RESX 释放后至少 120 毫秒内断电。

During power off, if LCD is in the Sleep Out mode, VDD and IOVDD must be powered down minimum 120msec after RESX has been released.

在断电期间, 如果 LCD 处于睡眠模式, VDDI 或 VDD 可以在 RESX 释放后至少 0 毫秒内断电。

During power off, if LCD is in the Sleep In mode, VDDI or VDD can be powered down minimum 0msec after RESX has been released.

CSX 可以在任何时间应用, 也可以永久接地。RESX 比 CSX 有优先权。

CSX can be applied at any timing or can be permanently grounded. RESX has priority over CSX.

注 1: 如果不符合电源顺序, 将不会对显示模块造成损害。

Note 1: There will be no damage to the display module if the power sequences are not met.

注 2: 在通电/断电序列期间, 显示面板上不会出现异常的可见效果。

Note 2: There will be no abnormal visible effects on the display panel during the Power On/Off Sequences.

注 3: 在开机顺序结束后到接收睡眠结束命令前, 显示屏上不会出现异常可见的效果。在接收 "睡眠" 命令和 "关机" 序列之间也是如此。

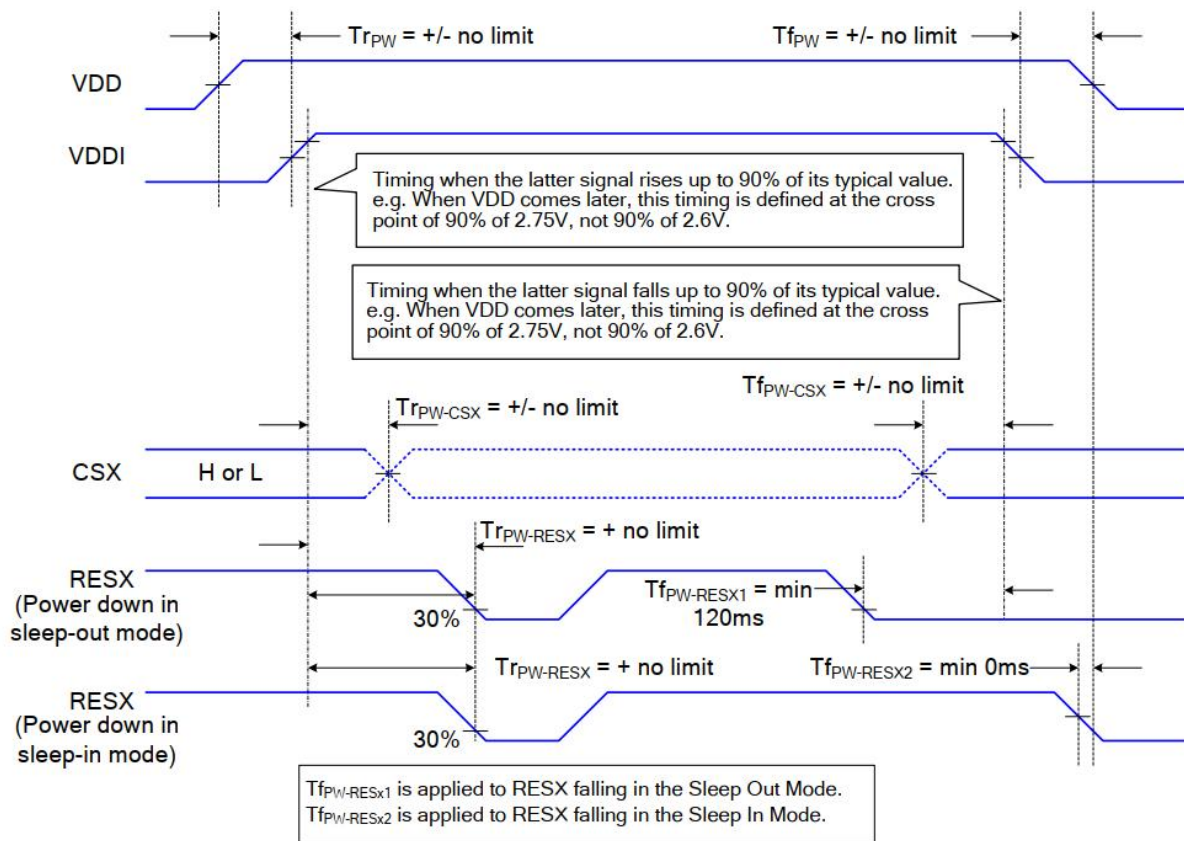
Note 3: There will be no abnormal visible effects on the display between end of Power On Sequence and before receiving Sleep Out command. Also between receiving Sleep In command and Power Off Sequence.

注 4: 如果 RESX 线在开机顺序中没有被主机保持稳定, 如下面的顺序所定义, 那么在主机开机顺序完成后, 有必要应用硬件复位 (RESX) 以确保正确操作。否则, 功能将无法保证。

Note 4: If RESX line is not held stable by host during Power On Sequence as defined in the sequence below, then it will be necessary to apply a Hardware Reset (RESX) after Host Power On Sequence is complete to ensure correct operation. Otherwise function is

not guaranteed.

电源开/关顺序如下图所示 The power on/off sequence is illustrated below



不受控制的断电是指在没有受控制的断电顺序的情况下取出电池。它既不会损坏模块，也不会损坏主机接口。

The uncontrolled power-off means a situation which removed a battery without the controlled power off sequence. It will neither damage the module or the host interface.

如果发生不受控制的断电，显示屏将变成空白，没有任何可见的影响。显示屏上没有任何可见的影响（空白显示），并保持空白，直到 "通电顺序 " 将其恢复。

If uncontrolled power-off happened, the display will go blank and there will not any visible effect on the display (blank display) and remains blank until "Power On Sequence" powers it up

七、可靠性测试 RELIABILITY TEST

7-1 温度和湿度 Temperature and Humidity

测试项目 TEST ITEMS	条件 CONDITIONS	注释 NOTE
高温储存 High Temperature Storage	Ta=+80 °C, 120hrs	
低温储存 Low Temperature Storage	Ta=-30 °C, 120hrs	
高温运行试验 High Temperature Operation	Ta=+70 °C, 120hrs	
低温运行试验 Low Temperature Operation	Ta=-30 °C, 120hrs	
高温高湿（运行测试） High Temperature and High Humidity (Operating)	Ta=+60 °C, 90%RH, 120hrs	

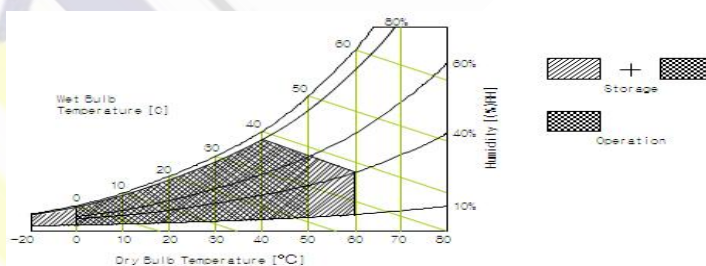
注释 Note:

1. 液晶驱动电压。由于液晶材料的特性，该电压随环境温度而变化。

Liquid Crystal driving voltage. Due to the characteristics of LC Material, this voltage varies with environmental temperature.

2. 温度和相对湿度范围如下图所示。湿球温度最高应为39℃。并且没有冷凝水。

Temperature and relative humidity range are shown in the figure below. Wet bulb temperature should be 39 °C max. and no condensation of water.



3. 产品经可靠性测试后，仅保证功能正常，无任何致命缺陷（不显示、线路缺陷、显示异常等）。

After the reliability test, the product only guarantee function normally without any fatal defect(non-display, line defect, abnormal display etc).

4. 所有显示判断均在面板温度恢复到室温2小时后进行

All display judgments are made after the panel temperature returns to room temperature for 2 hours

5. Ta: 环境温度

Ta: Ambient temperature

7-2 冲击和振动 Shock and Vibration

测试项目 TEST ITEMS	条件 CONDITIONS
包装冲击(非操作) Packing Shock (Non-Operation)	● Shock level:980m/s² ● Waveform:1/2 Sine wave,6msec ● ±X, ±Y ±Z, each axis 1 times
包装振动(非操作) Packing Vibration (Non-Operation)	● Frequency range:8-33.3HZ ● Stoke:1.0mm ● Sweep: 10Hz-50Hz ● x, y, z 2 hours for each direction

7-3 静电放电测试 Electrostatic Discharge

测试项目 TEST ITEMS	条件 CONDITIONS
ESD (Non-operation)	150pF, 330 Ω, Contact ±4KV, Air : ±8KV. Note 1 200pF, 0 Ω, ±200V Contact test. Note 2

测量点 Measure Point:

1. LCD玻璃和金属边框
LCD glass and metal bezel
2. 连接器引脚
IF connector pins

八、处理和注意事项 HANDDLING & CAUTIONS

8-1 操作注意事项 Caution For Operation

◆由于液晶模组是玻璃材质，请勿对其施加强烈的机械冲击或静载荷。请小心搬运，因为冲击、振动和粗心的搬运可能会严重影响产品。如果从高处坠落或受到强烈冲击，玻璃可能碎了。

Since the LCM is made of glass, do not apply strong mechanical impact or static load onto it. Handling with care since shock, vibration, and careless handling may seriously affect the product. If it falls from a high place or receives a strong shock, the glass maybe broken.

◆在规定的电压限制内驱动 LCM 是必不可少的，因为高于限制的电压会导致 LCM 的寿命缩短。由直流引起的电化学反应会导致 LCM 出现不良劣化，因此应避免使用直流驱动。

It is indispensable to drive the LCM within the specified voltage limit since the higher voltage than the limit causes LCM's life shorter. An electro-chemical reaction due to DC causes undesirable deterioration of the LCM so that the use of DC drive should avoid.

◆请勿在电源开启时将 LCM 连接到系统或从系统断开连接。

Do not connect or disconnect the LCM to or from the system when power is on.

◆切勿在高温高湿的异常条件下使用 LCM。

Never use the LCM under abnormal conditions of high temperature and high humidity.

◆当暴露于剧烈的温度波动（热到冷或冷到热）时，LCM 可能会受到影响；具体来说，从冷到热的剧烈温度波动会在 LCM 表面产生露水，这可能会影响 LCM 上偏振片的运行。

When expose to drastic fluctuation of temperature(hot to cold or cold to hot), the LCM may be affected; specifically, drastic temperature fluctuation from cold to hot, produces dew on the LCM's surface which may affect the operation of the polarizer on the LCM.

◆在低于工作温度范围的温度下，响应时间将极度延迟，另一方面，在高于其工作范围的温度下，LCM 可能会变黑。然而，这些现象并不意味着 LCM 出现故障或故障。一旦温度恢复到正常运行的推荐温度范围，LCM 将恢复正常运行。

Response time will be extremely delay at lower temperature than the operating temperature range and on the other hand LCM may turn black at temperature above its operational range. However those phenomenon do not mean malfunction or out of order with the LCM. The LCM will revert to normal operation once the temperature returns to the recommended temperature range for normal operation.

◆为防止LCD产生图像残留，在使用常黑面板时不要长时间显示固定图案。如果LCD需要显示固定图案，建议时间少于 2 分钟或者更短的时间内刷新显示内容一次或多次。强烈建议使用黑色图像或移动图像作为屏幕保护程序。

In order to prevent the LCD from producing image retention, do not display a fixed pattern for a long time when using a normally black panel. If the LCD needs to display a fixed pattern, it is recommended to refresh the display one or more times in less

than 2 minutes or less. It is strongly recommended to use a black image or moving image as a screen saver.

◆强烈的光照会导致 LCD 偏光片和彩色滤光片退化。 不准在强光或高温高湿下长期存放或直接运行。

Strong sunlight can cause LCD polarizers and color filters to degrade. Long-term storage or direct operation under strong light or high temperature and humidity is not allowed.

8-2 防静电措施 Caution Against Static Charge

◆LCM 使用 C-MOS LSI 驱动器，因此建议客户将任何未使用的输入端连接到 Vdd 或 Vss，上电前不要输入任何信号，并将您的身体、工作/装配区、装配设备接地 防止静电。

The LCM use C-MOS LSI drivers, so customers are recommended that any unused input terminal would be connected to Vdd or Vss, do not input any signals before power is turn on, and ground you body, work/assembly area, assembly equipments to protect against static electricity.

◆缓慢去除保护膜，保持去除方向与面板表面不垂直约 30 度，如有可能，在离子风机等 ESD 控制装置下，工作室内湿度应保持在 50%RH 以上，以减少静电风险

Remove the protective film slowly, keeping the removing direction approximate 30-degree not vertical from panel surface, if possible, under ESD control device like ion blower, and the humidity of working room should be kept over 50%RH to reduce the risk of static charge.

◆避免使用合成纤维制成的工作服。 我们推荐棉质衣服或其他经过导电处理的纤维。

Avoid the use work clothing made of synthetic fibers. We recommend cotton clothing or other conductivity-treated fibers.

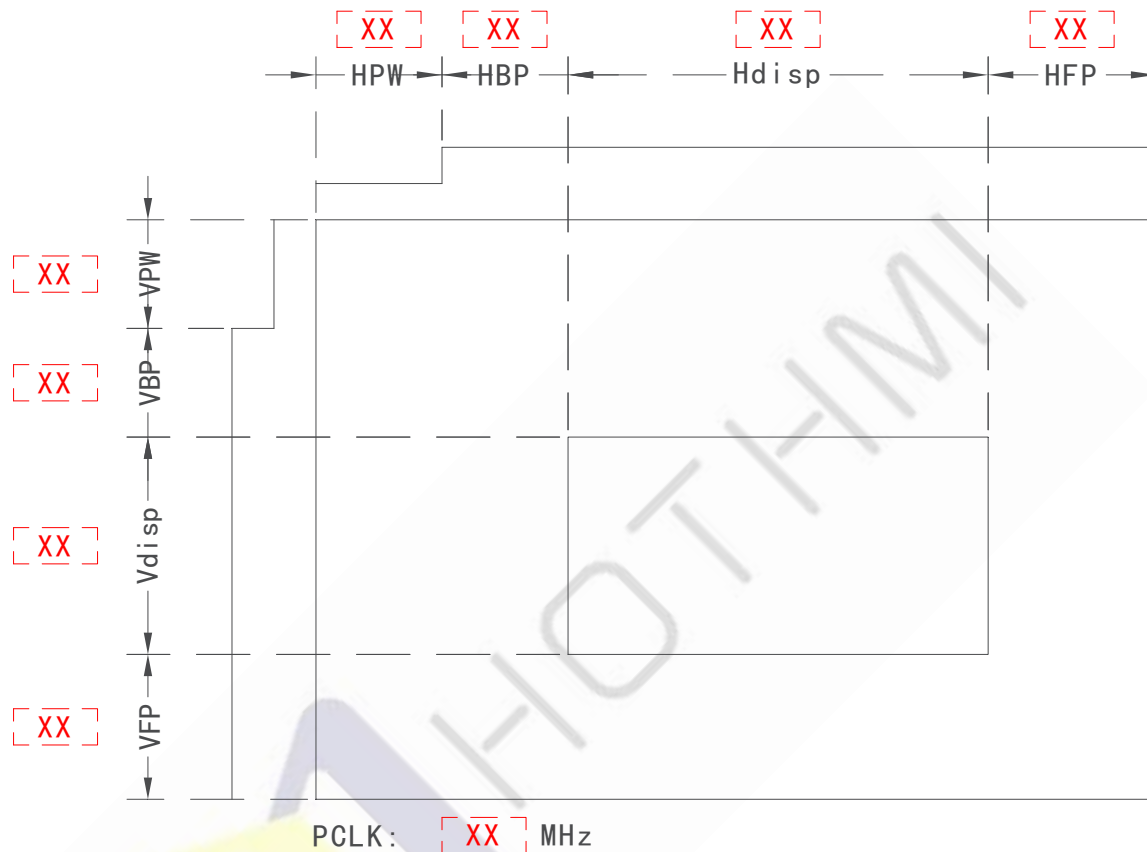
◆在处理 LCM 时，请戴上不带电材料的手套。 对地导电手腕和对地导电鞋是必需的

In handling the LCM, wear non-charged material gloves. And the conducting wrist to the earth and the conducting shoes to the earth are necessary

九、初始化代码 Initialization code

说明：这些参数值仅供参考，可能并不完全适用于所有软件平台，
 请根据您使用的平台优化参数。

Description : These parameter values are for reference only and
 may not be fully applicable to all software platforms, please
 optimise the parameters according to the platform you are using.



```
Void Panel_Initial_code(void)
{
```

```
//=====上电复位操作=====//
```

```
LCD_RESET=1;
```

```
Delays(1);          //Delay 1ms
```

```
LCD_RESET=0;
```

```
Delays(10);         //Delay 10ms
```

```
LCD_RESET=1;
```

```
Delays(120);        //Delay 120ms
```

```
//=====//
```

```
//MIPI 接口初始化代码待确认 MIPI interface initialisation code to be confirmed
```

```
}
```

文档修订记录 Document revision history :

版本 Version	日期 DATE	修改说明 Modify description	编著 Editorial
0-0	2025-01-17	初次编制 First compilation.	YL

— — 结束 — —
— — END — —